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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM FILING DATE CLASS SUBCLASS GAU EXAMINER
10044364 01/11/2002 711 167 2186 M. Kim T. Ho

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**CONTINUING DATA VERIFIED:

THIS APPLICATION IS A CIP OF 09/896,043 06/28/2001 TA

** FOREIGN APPLICATIONS VERIFIED: No

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiner's initials		<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	ATTORNEY DOCKET NO 10012380-1
TITLE : Managing latencies in accessing memory of computer systems U.S.DEP.T. OF COMM./PAT.& TM-PTO-436L(Rev. 12-94)			

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
TERMINAL DISCLAIMER		Application Examiner		
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